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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/663,911
Filing Date	September 17, 2003
First Named Inventor	Frampton E. ELLIS III
Art Unit	2153
Examiner Name	Unassigned
Attorney Docket Number	313449-P0017 (C1)

Sheet	1	of	2
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ² Number ³ Kind Code ⁵ (if known)				
		EP 0 840216	5/1998	Chung et al.		

Examiner
Signature /Lashanya Nash/ (09/07/2008)

Date	
Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				T ²
		Regenold et al., "A Single-Chip Multiprocessor DSP Solution for Communication Applications", ASIC Conference and Exhibit 1994, pp. 437-440.				
		Geppert, L., "Solid State [Trend/Development], IEEE Spectrum, Vol. 33, Issue 1, 1996, pp. 51-55.				
		The American Heritage College Dictionary 4th Ed. - definition of "firewall".				
		White, "Covert Distributed Processing with Computer Viruses", Advances in Cryptology, Crypto 89, Springer LNCS, v. 435, pp. 616-619.				
		Wilkinson, et al., "Parallel Programming", Prentice Hall, 1998.				
		Slater, "The Microprocessor Today", IEEE Micro 1996, pp. 32-44.				
Examiner Signature	/Lashanya Nash/ (09/07/2008)			Date Considered		

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